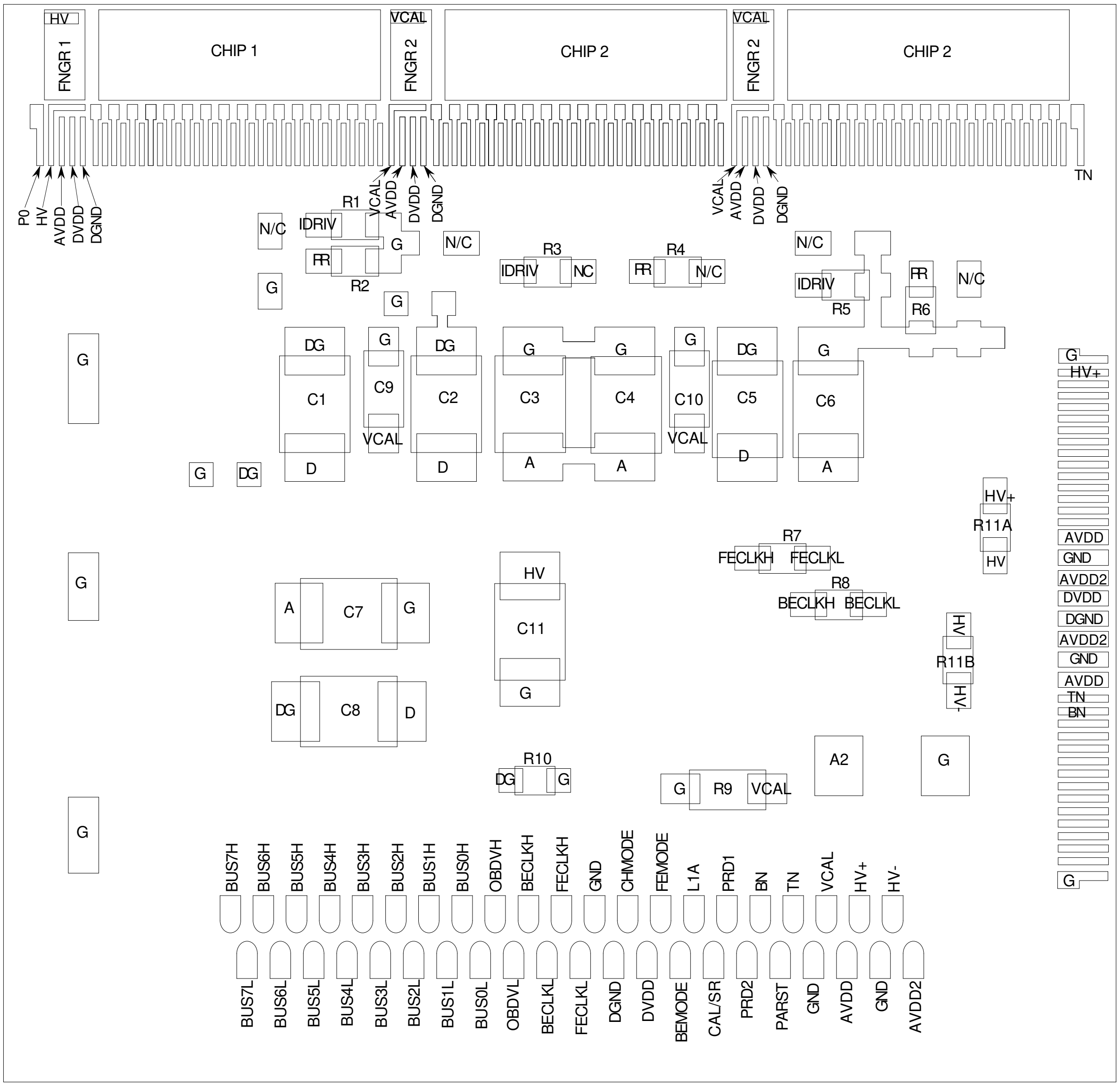

HV
FNGR 1
CHIP 1
VCAL
FNGR 2
CHIP 2
VCAL
FNGR 2
CHIP 2
TN
P0
HV
AVDD
DVDD
DGND
VCAL
AVDD
DVDD
DGND
VCAL
AVDD
DVDD
DGND
N/C
IDRIV
R1
G
N/C
RR
R2
G
G
R3
IDRIV
NC
R4
RR
N/C
N/C
IDRIV
R5
RR
R6
N/C
G
G
HV+
DG
G
DG
G
G
G
DG
G
C1
C9
C2
C3
C4
C10
C5
C6
VCAL
VCAL
G
DG
D
D
A
A
D
A
HV+
R11A
HV
AVDD
GND
AVDD2
DVDD
DGND
AVDD2
GND
AVDD
TN
BN
R7
FECLKH
FECLKL
R8
BECLKH
BECLKL
G
HV
A
C7
G
C11
HV
R11B
HV-
DG
C8
D
G
A2
G
R10
DG
G
G
R9
VCAL
G
G
BUS7H
BUS6H
BUS5H
BUS4H
BUS3H
BUS2H
BUS1H
BUS0H
OBDVH
BECLKH
FECLKH
GND
CHMODE
FEMODE
L1A
PRD1
BN
TN
VCAL
HV+
HV-
BUS7L
BUS6L
BUS5L
BUS4L
BUS3L
BUS2L
BUS1L
BUS0L
OBDVL
BECLKL
FECLKL
DGND
DVDD
BEMODE
CAL/SR
PRD2
PARST
GND
AVDD
GND
AVDD2