# Hybrid 6p30 Traveler

September 8, 1999

## 1 Connectivity

Repairs on shorts:

Fixes for opens:

## 2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 6P30 has die: 085-046 085-047 085-048 085-050 085-052 085-054

## 3 Bad Channels

The following channels were bad or exceedingly noisy:
231,232

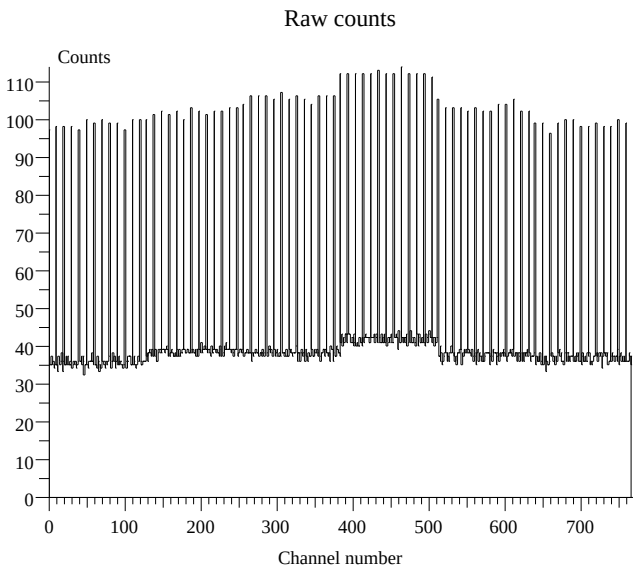
## 4 Comments

No comments.

# 5 Pedestal

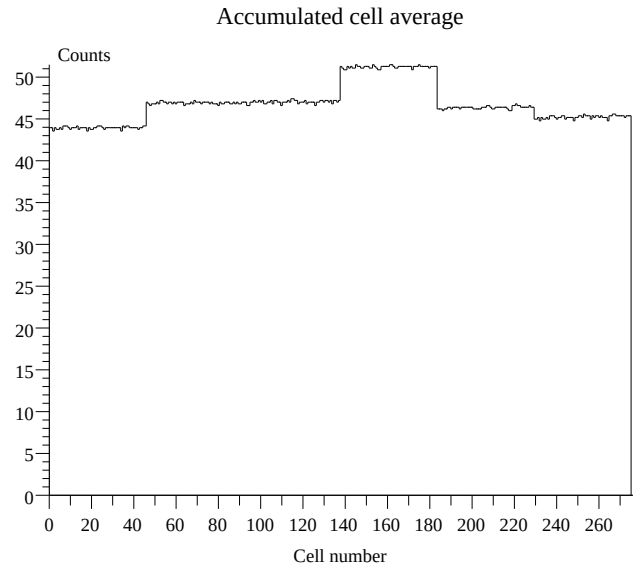
The following plot shows the pedestals vs. channel number

**Pedestal**




The following plot shows the average pedestal vs. pipeline cell ID. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 on chip 1 ia at x=5, chip 2 at x=49, chip 3 at x=97, etc.

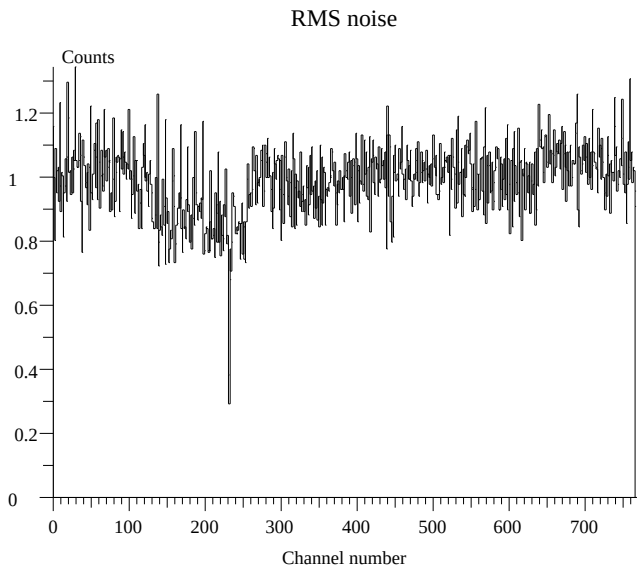
**Pedestal vs. Pipeline Cell ID**



# 6 Noise

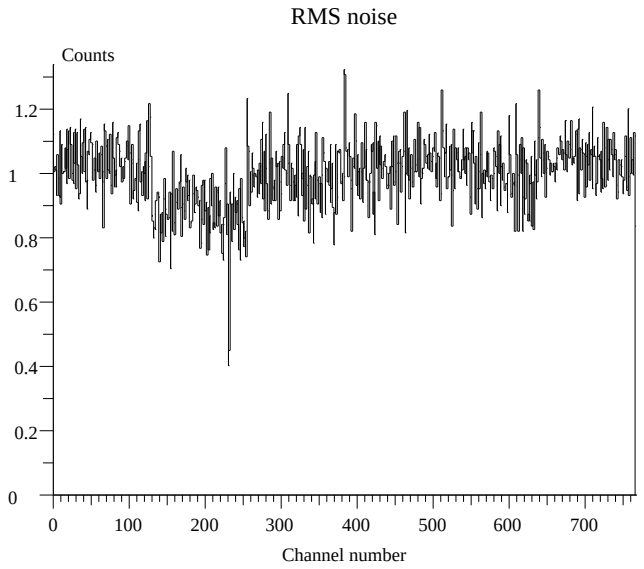
The following plot shows the single cell RMS noise:

**RMS Noise**

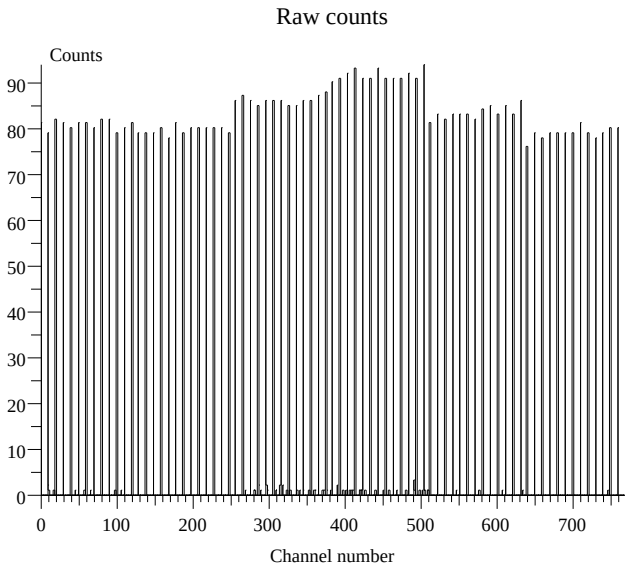



The following plot show the multi-cell RMS noise:

**Multi-Cell Noise**



# 7 Dynamic Threshold Subtraction

The following plot shows the readout of channels with charge injection with the dynamic threshold subtraction turned on:

**Dynamic Threshold Subtraction**