Hybrid 7p50 Traveler

September 24, 1999

1 Connectivity

Repairs on shorts:

Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 7P50 has die: 120-035 120-036 120-041 087-054 087-057 087-067 087-093

3 Bad Channels

The following channels were bad or exceedingly noisy: 852

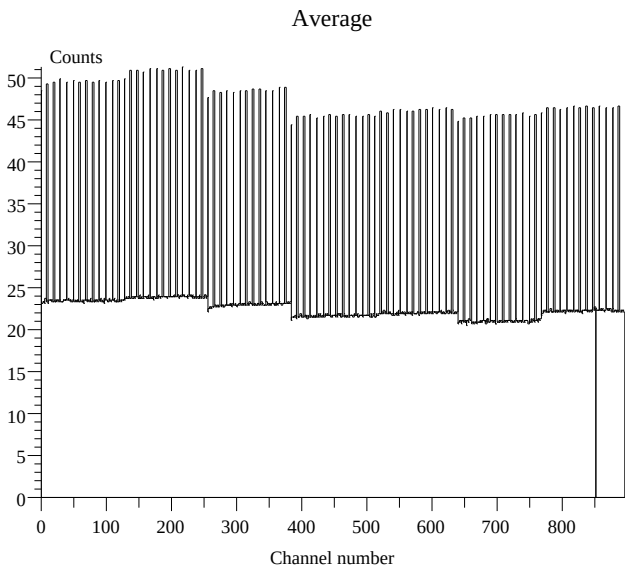
4 Comments

No comments.

5 Pedestal

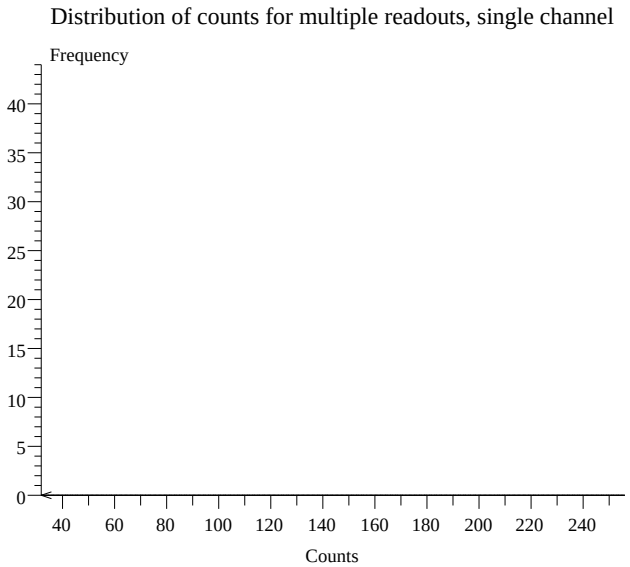
The following plot shows the pedestals vs. channel number

Pedestal



The following plot shows the average pedestal vs. pipeline cell ID. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 on chip 1 ia at x=5, chip 2 at x=49, chip 3 at x=97, etc.

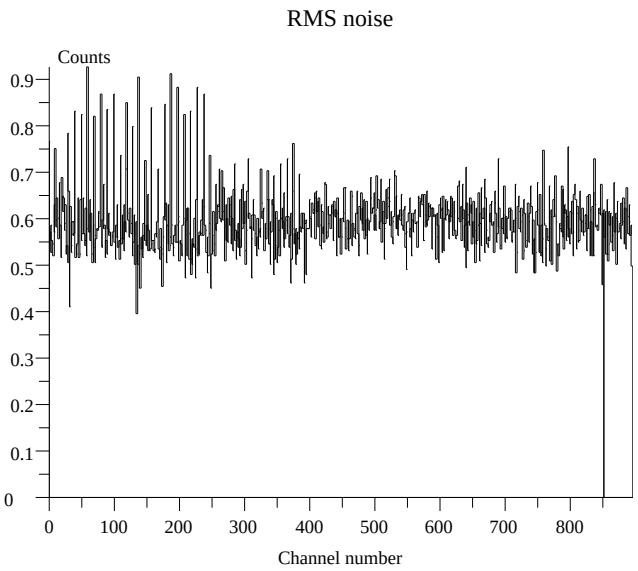
Pedestal vs. Pipeline Cell ID



6 Noise

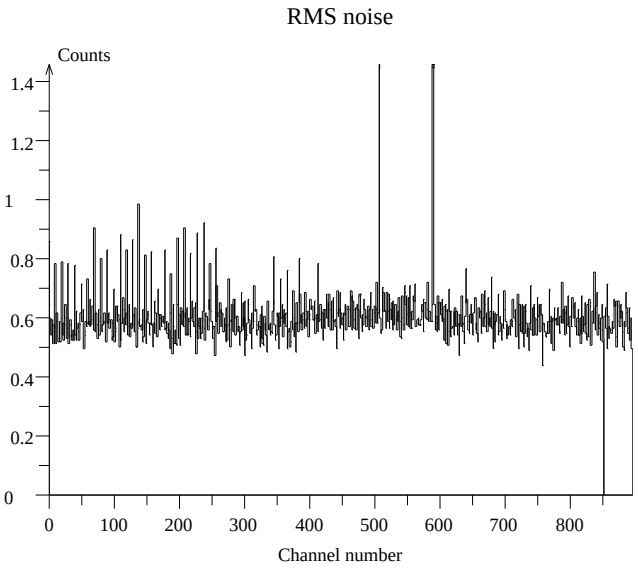
The following plot shows the single cell RMS noise:

RMS Noise

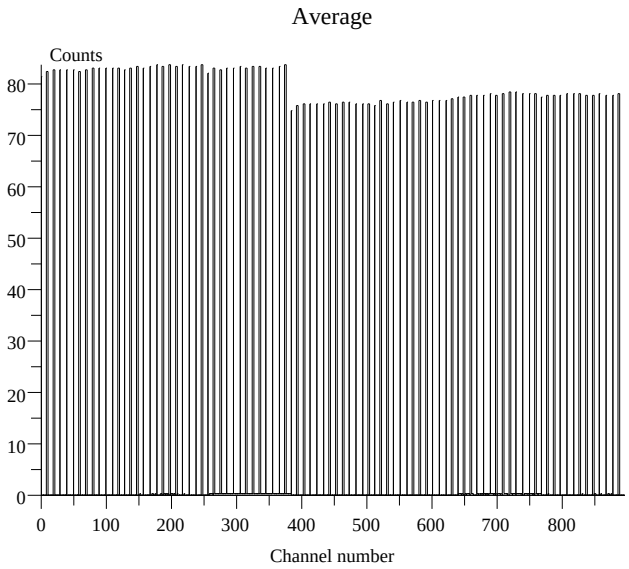


The following plot show the multi-cell RMS noise:

Multi-Cell Noise



7 Dynamic Threshold Subtraction

The following plot shows the readout of channels with charge injection with the dynamic threshold subtraction turned on:

Dynamic Threshold Subtraction