Hybrid 7z1 Traveler

September 27, 1999

1 Connectivity

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 7Z1 has die: 115-019 115-050 001-014 114-007 114-016 114-020 114-045

3 Bad Channels

Channel 42 dead.
Noisy when cell cycling: 170,416,578.

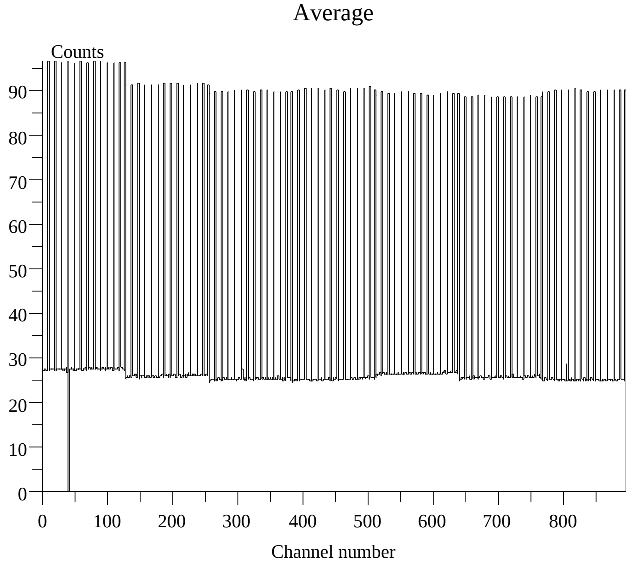
4 Comments

Vcal set to 2.0 V.
Bit 9 checked.

5 Pedestal

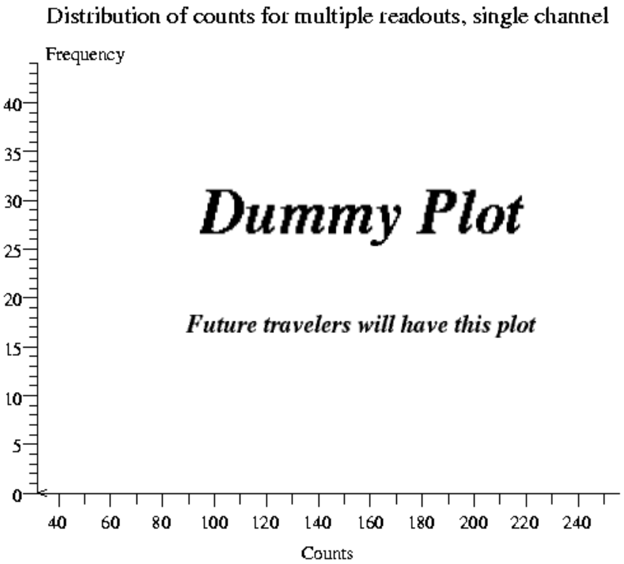
The following plot shows the pedestals vs. channel number

Pedestal



The following plot shows the average pedestal vs. pipeline cell ID. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 on chip 1 ia at x=5, chip 2 at x=49, chip 3 at x=97, etc.

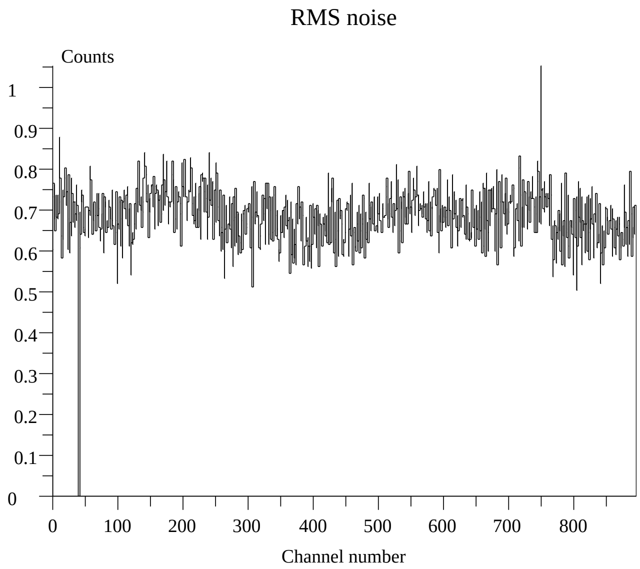
Pedestal vs. Pipeline Cell ID



6 Noise

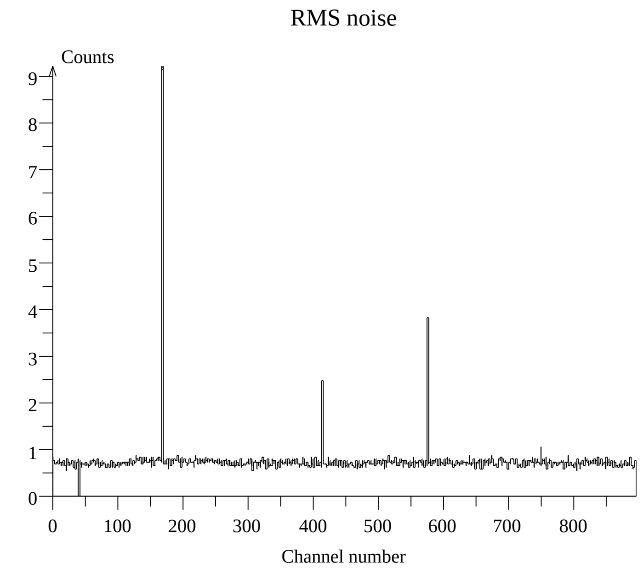
The following plot shows the single cell RMS noise:

RMS Noise



The following plot show the multi-cell RMS noise:

Multi-Cell Noise



7 Dynamic Threshold Subtraction

The following plot shows the readout of channels with charge injection with the dynamic threshold subtraction turned on:

Dynamic Threshold Subtraction