Hybrid 7z123 Traveler

July 19, 2000

1 Connectivity

Repairs on shorts:
Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 7Z123 has die: 222-063 222-066 222-068 222-072 222-073 222-076 222-080

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

```
% Hybrid 7z123 classification info. Created 09:23:14 06/29/00

Hybrid 7z123 quality: fair*
Chip classification: fair fair fair good fair fair fair*
Bad channels (chip-channel): 0-23 0-70 2-105 4-34 6-21 6-48 6-117
Bad cells: none
Random bad capacitors (chip-cell-channel): 0-10-66 1-12-29 5-25-0
Channels at pedestal needed for DCMS: 30.8 35.6 34.6 35.7 30.6 32.6 31.7
```

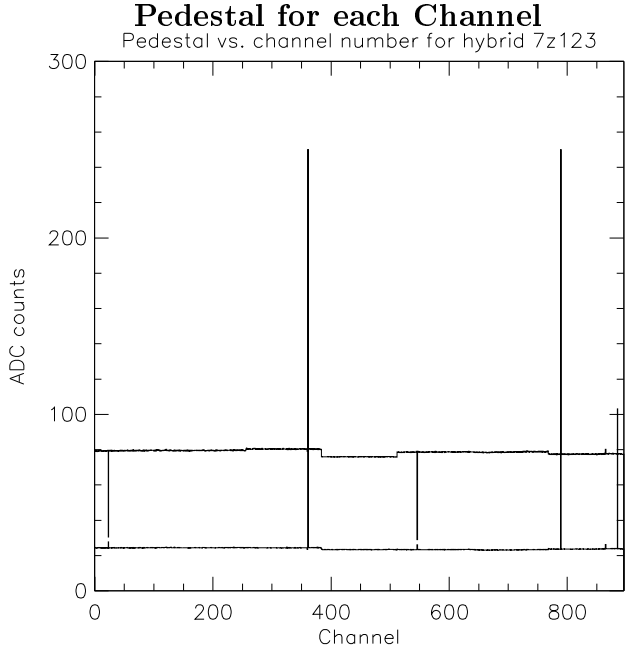
4 Comments

Encapsulated. FE DV finger gold bonds. HV tested.

5 Pedestal

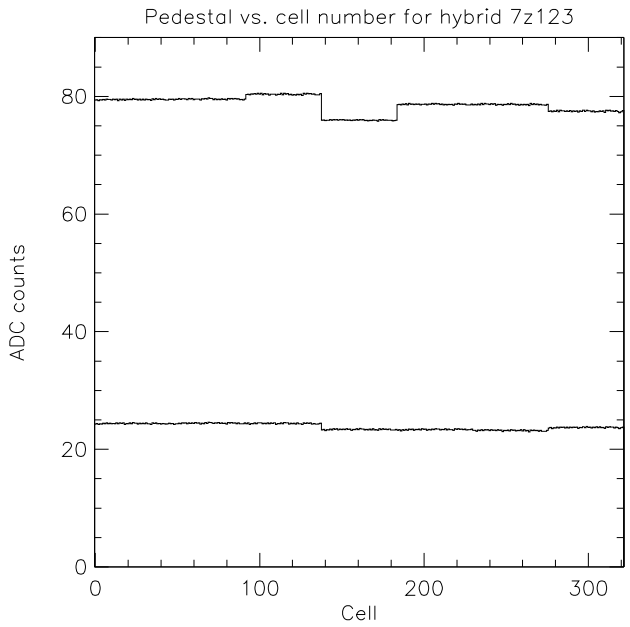
The following plot shows the pedestals vs. channel number with and without charge injection:

Pedestal for each Channel



The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.

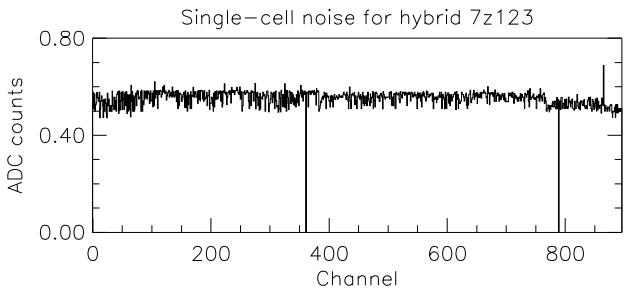
Pedestal vs. Pipeline Cell ID

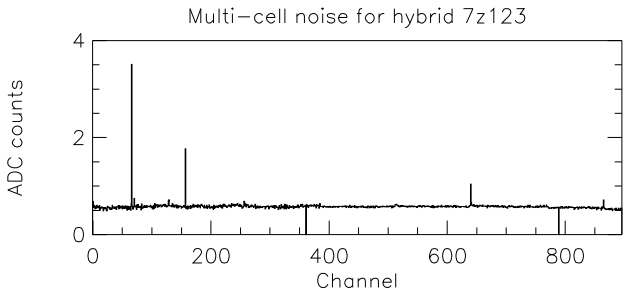


6 Noise

The following plots show the single-cell and multi-cell RMS noise:

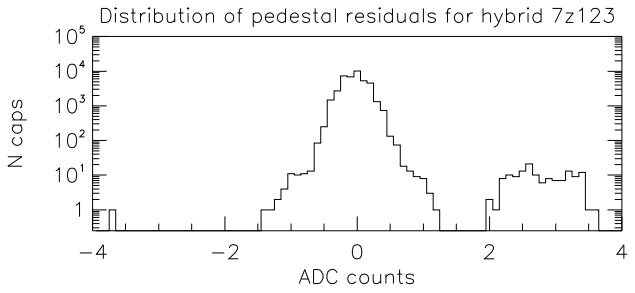
RMS Noise

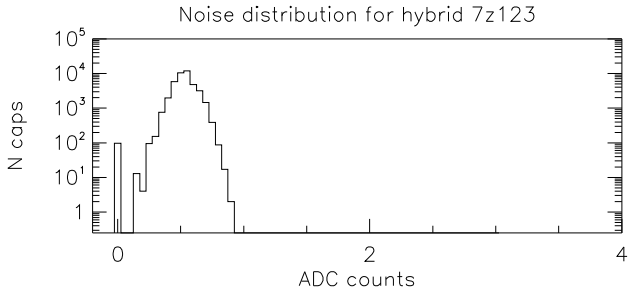




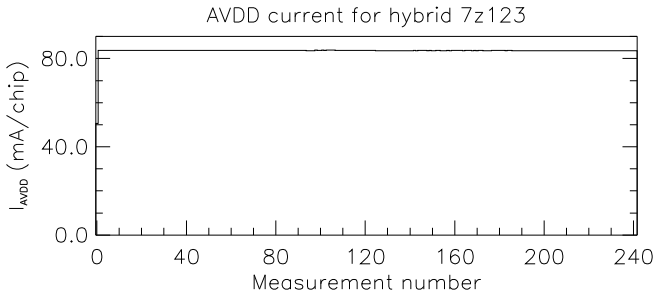
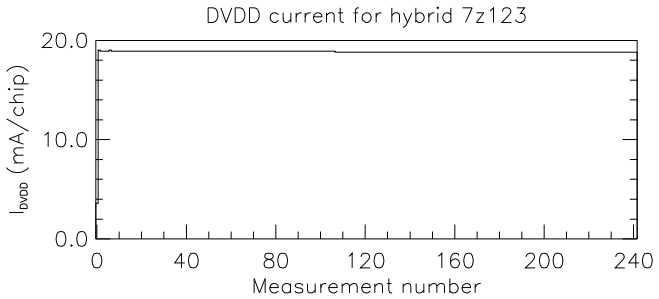
The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise





7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in