Hybrid 8j119 Traveler

January 11, 2000

1 Connectivity

Repairs on shorts:

Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 8J119 has die: 097-021 097-022 097-023 097-024 197-072 197-073 197-074 197-076

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

```
% Hybrid 8j119 classification info. Created 14:40:28 01/07/00

Hybrid 8j119 quality: fair
Chip classification: good fair fair good fair good fair good
Bad channels (chip-channel): 6-75
Bad cells: none
Random bad capacitors (chip-cell-channel): 1-1-28 1-1-29 1-14-27
   1-14-28 1-14-29 1-15-29 1-2-29 1-27-29 1-3-28 1-3-29 1-40-28
   1-40-29 1-41-29 1-44-28 1-44-29 1-45-29 1-5-28 1-5-29 1-6-29
   2-4-65 4-40-98
Channels at pedestal needed for DCMS: 36.7 35.6 33.7 34.7 32.7 32.6 32.6 32.6
```

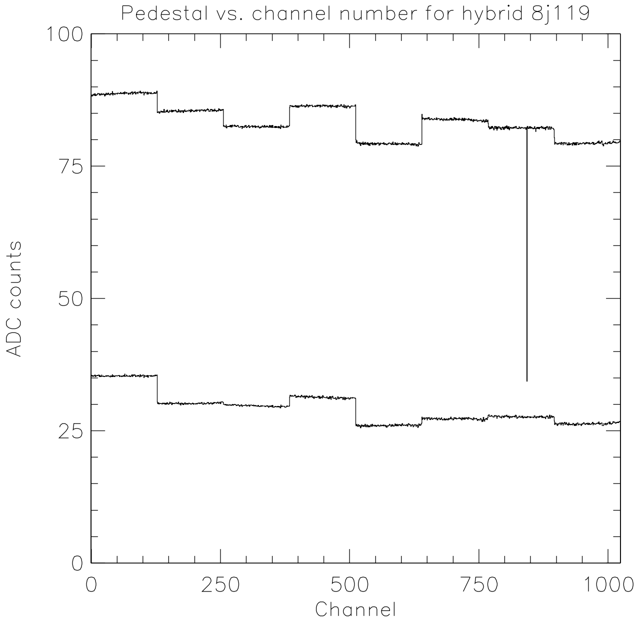
4 Comments

No comments.

5 Pedestal

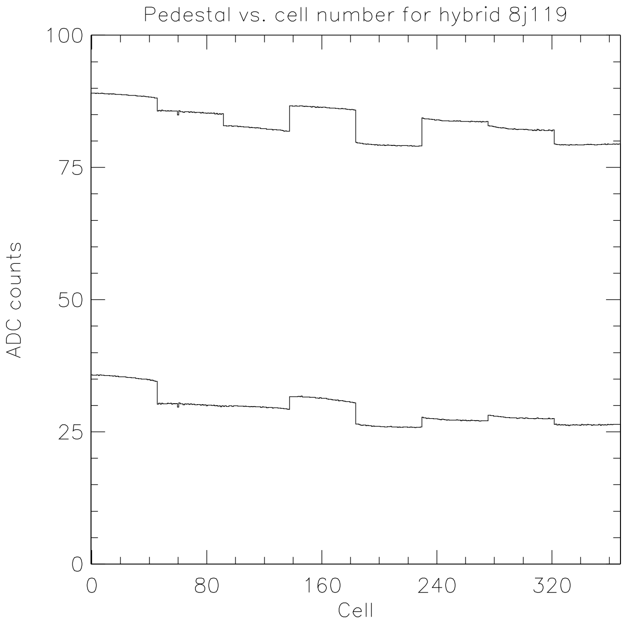
The following plot shows the pedestals vs. channel number with and without charge injection:

Pedestal for each Channel



The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.

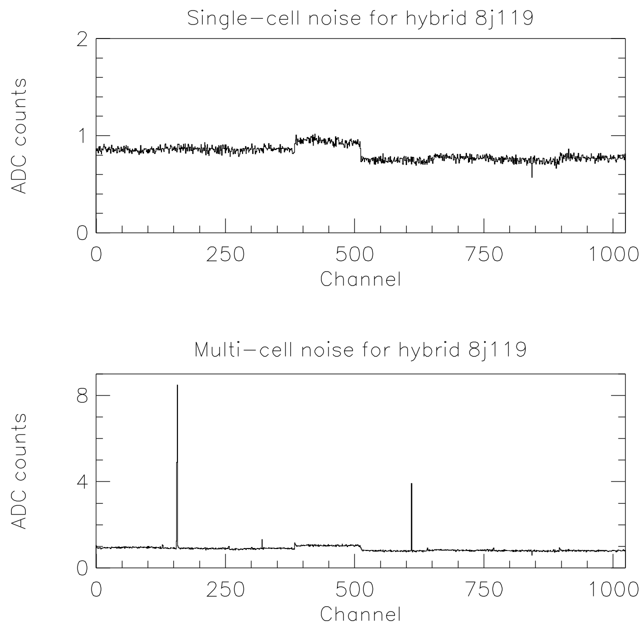
Pedestal vs. Pipeline Cell ID



6 Noise

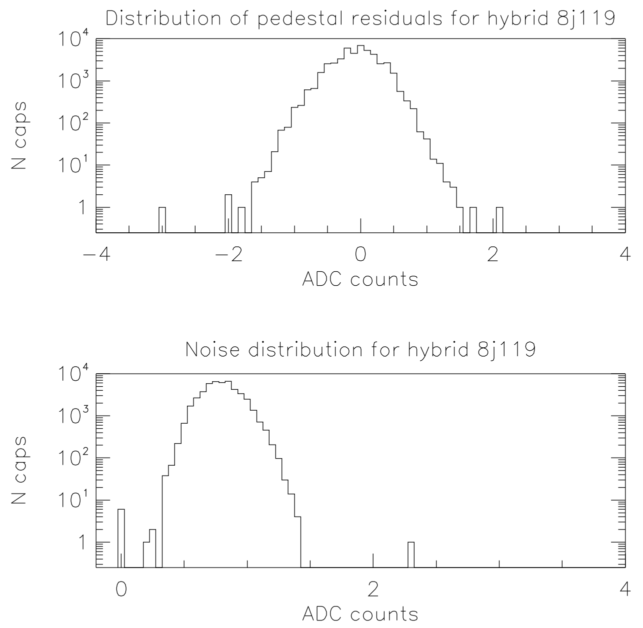
The following plots show the single-cell and multi-cell RMS noise:

RMS Noise



The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise



7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in