Hybrid 8j12 Traveler

November 23, 1999

1 Connectivity

Repairs on shorts:

Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 8J12 has die: 064-037 064-038 064-040 064-041 058-043 058-045 058-046 058-047

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

```
% Hybrid 8j12 classification info. Created 08:51:14 11/12/99

Hybrid 8j12 quality: fair
Chip classification: good fair good fair fair good fair good
Bad channels (chip-channel): 1-63
Bad cells: none
Random bad capacitors (chip-cell-channel): 3-8-20 4-23-125 6-17-60
   6-44-73
Channels at pedestal needed for DCMS: 34.6 33.7 35.5 35.6 35.7 32.5 33.7 35.6
```

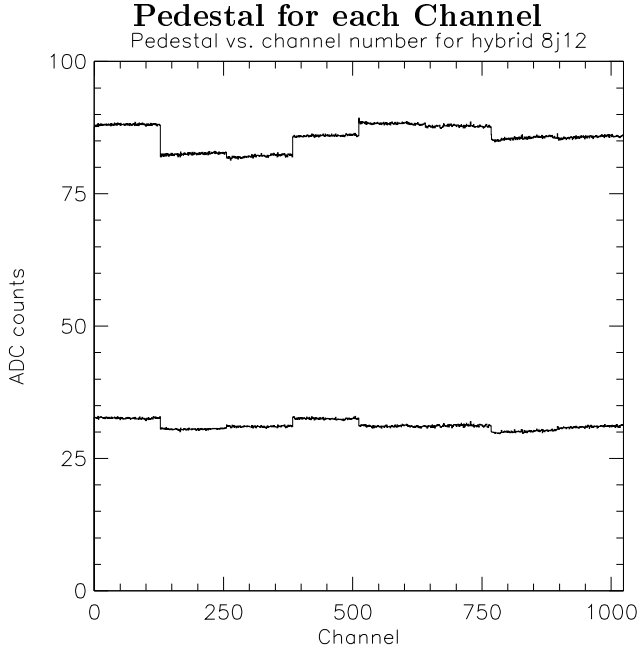
4 Comments

No comments.

5 Pedestal

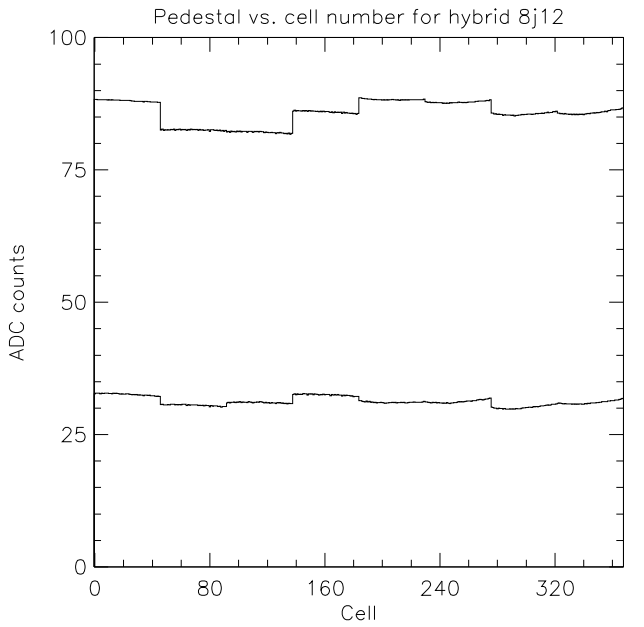
The following plot shows the pedestals vs. channel number with and without charge injection:

Pedestal for each Channel



The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.

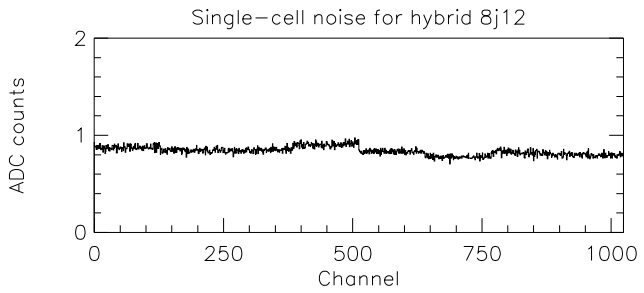
Pedestal vs. Pipeline Cell ID

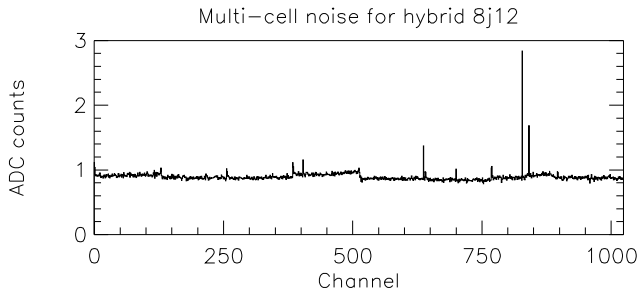


6 Noise

The following plots show the single-cell and multi-cell RMS noise:

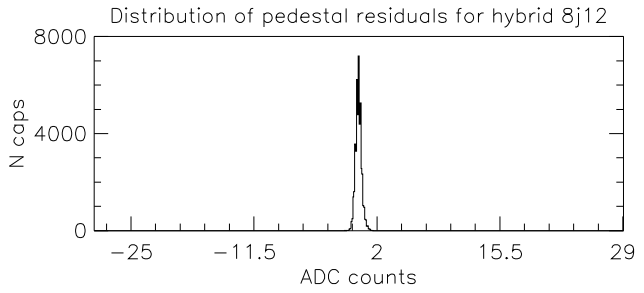
RMS Noise

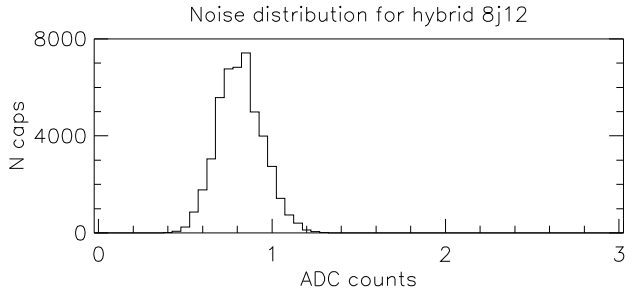




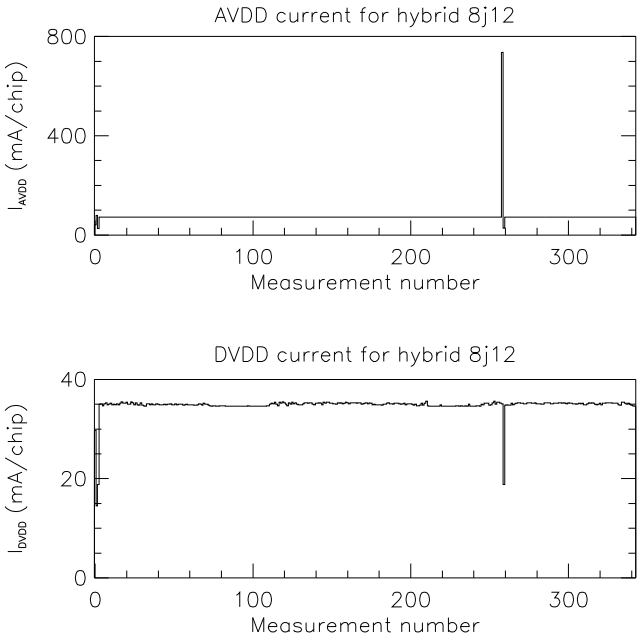
The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise





7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in