Hybrid 8j132 Traveler

February 2, 2000

1 Connectivity

Repairs on shorts:

Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 8J132 has die: 197-049 197-050 197-051 197-053 199-119 199-120 199-121 199-122

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

```
% Hybrid 8j132 classification info. Created 11:34:25 02/02/00

Hybrid 8j132 quality: fair
Chip classification: fair fair good fair good good good good
Bad channels (chip-channel): 3-67
Bad cells: none
Random bad capacitors (chip-cell-channel): 0-10-103 1-0-124 1-13-123
   1-13-124 1-13-125 1-14-123 1-14-124 1-14-125 1-15-123 1-15-124 1-15-125
   1-16-123 1-16-124 1-17-124 1-2-124 1-26-123 1-26-124 1-27-123 1-27-124
   1-27-125 1-28-123 1-28-124 1-28-125 1-40-124 1-42-124 1-44-124 1-6-124
Channels at pedestal needed for DCMS: 30.7 31.6 30.6 30.1 33.6 32.6 33.7 31.7
```

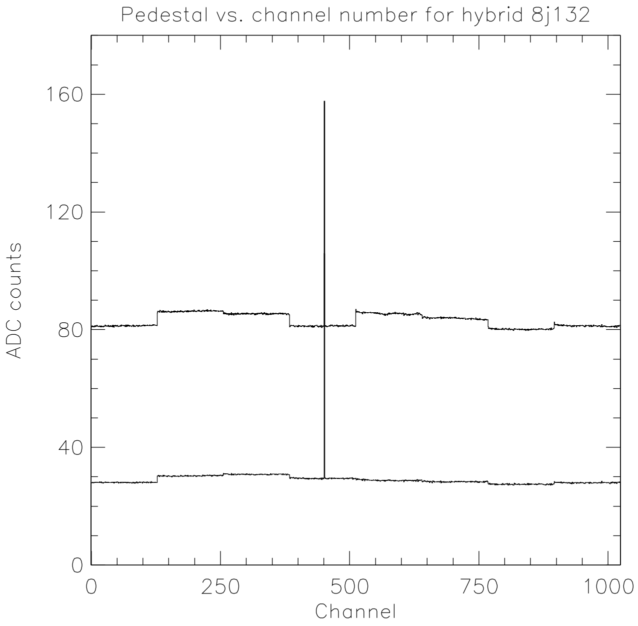
4 Comments

No comments.

5 Pedestal

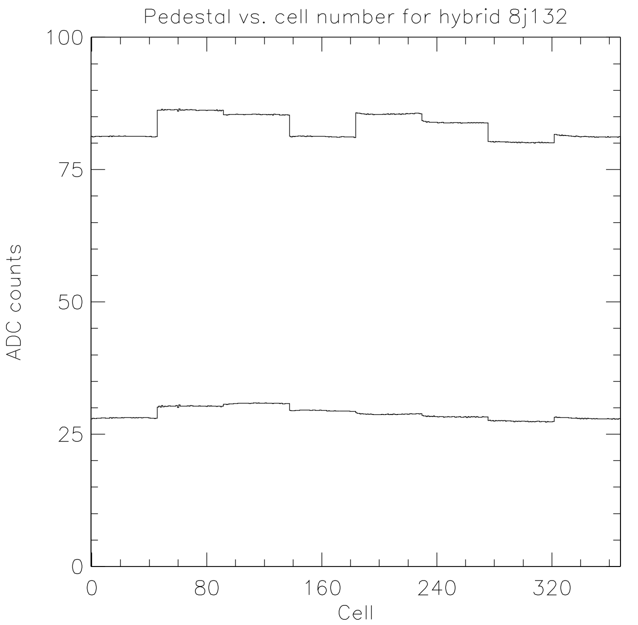
The following plot shows the pedestals vs. channel number with and without charge injection:

Pedestal for each Channel



The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.

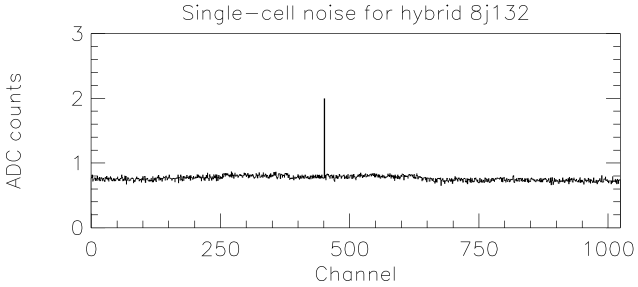
Pedestal vs. Pipeline Cell ID

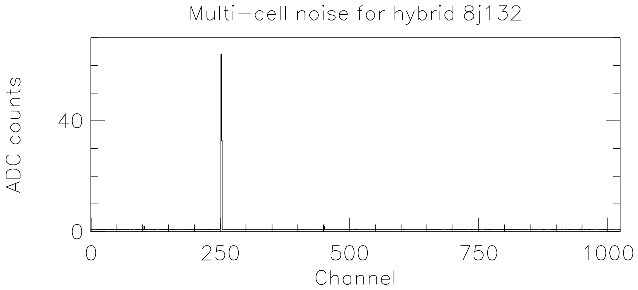


6 Noise

The following plots show the single-cell and multi-cell RMS noise:

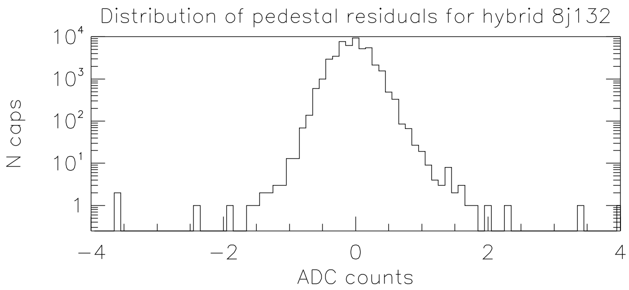
RMS Noise

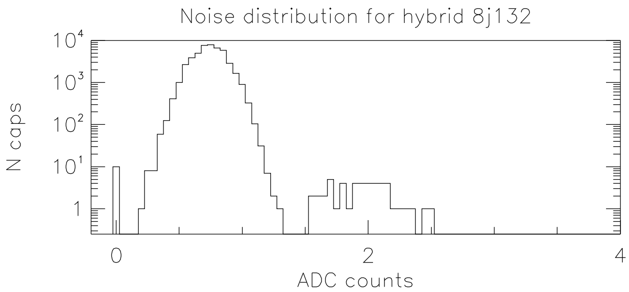




The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise





7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in