Hybrid 8j154 Traveler

January 18, 2000

1 Connectivity

Repairs on shorts:

Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 8J154 has die: 202-086 202-087 202-089 202-090 215-041 215-042 205-017 205-018

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

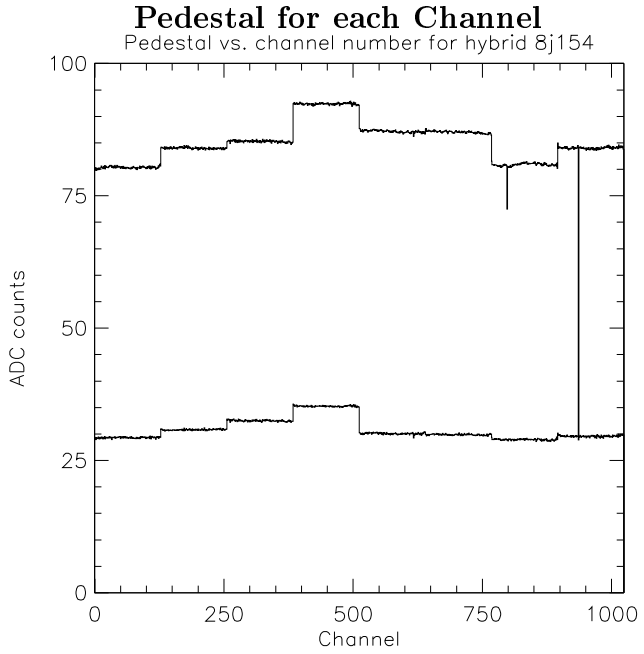
```
% Hybrid 8j154 classification info. Created 16:06:32 01/14/00

Hybrid 8j154 quality: fair
Chip classification: good fair fair good good fair fair fair
Bad channels (chip-channel): 5-67 6-30 7-39 7-40
Bad cells: none
Random bad capacitors (chip-cell-channel): 1-25-116 1-25-8 2-15-2
Channels at pedestal needed for DCMS: 32.7 33.6 33.6 32.7 29.7 30.7 28.8 30.6
```

4 Comments

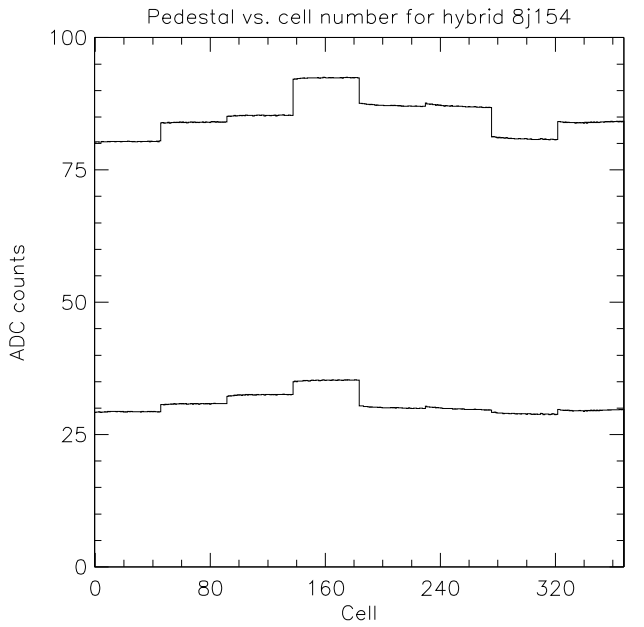
No comments.

5 Pedestal

The following plot shows the pedestals vs. channel number with and without charge injection:



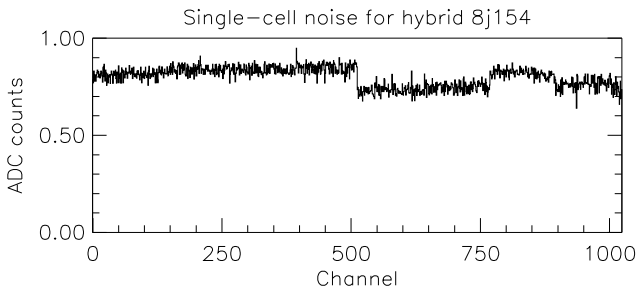
The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.

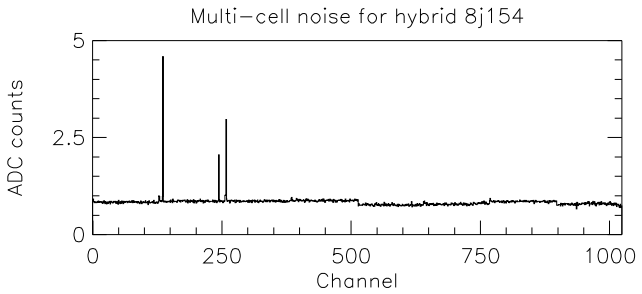


6 Noise

The following plots show the single-cell and multi-cell RMS noise:

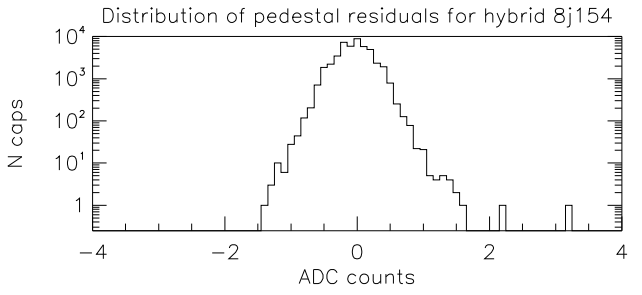
RMS Noise

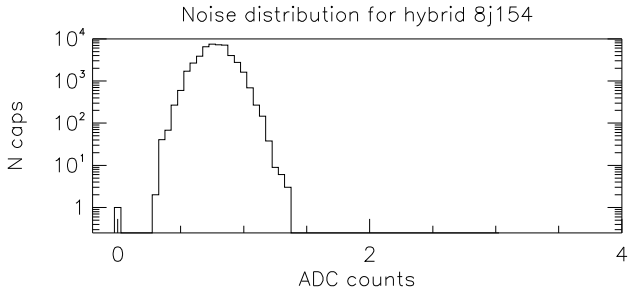




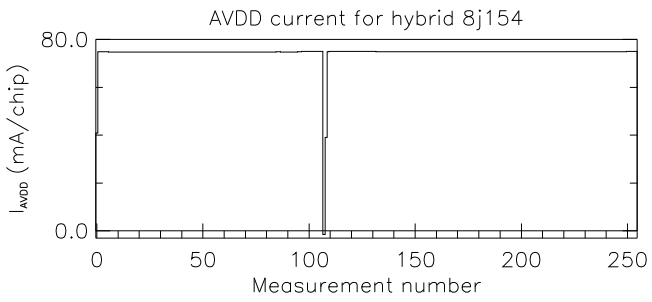
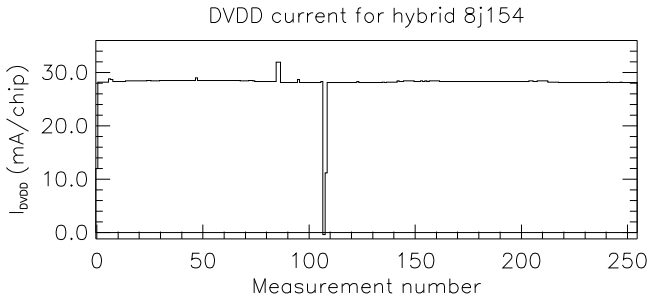
The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise





7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in