Hybrid 8j19 Traveler

November 30, 1999

1 Connectivity

Repairs on shorts:

Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 8J19 has die: 063-002 063-006 063-007 063-008 052-006 052-007 052-010 052-012

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

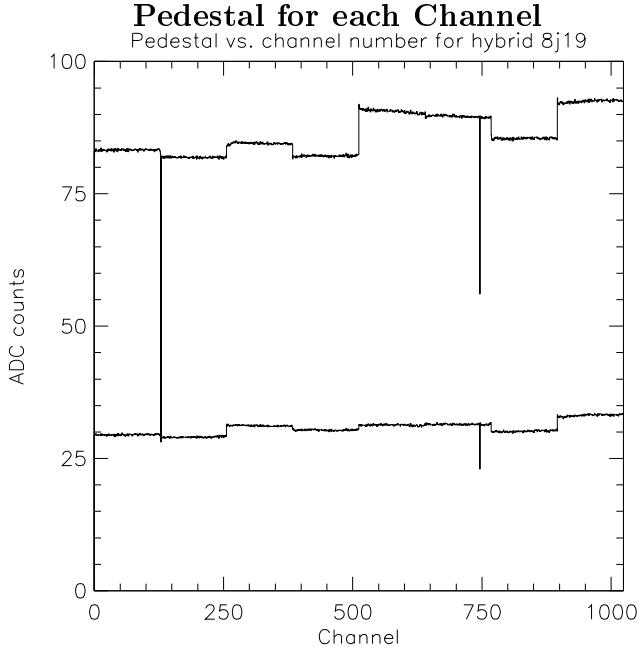
```
% Hybrid 8j19 classification info. Created 10:13:07 11/29/99

Hybrid 8j19 quality: bad
Chip classification: good fair good good good fair fair bad
Bad channels: N_bad_total = 130 (too many to list all of them)
Bad cells: none
Random bad capacitors (chip-cell-channel): 1-12-68 6-27-26 7-9-77
Channels at pedestal needed for DCMS: 35.6 34.7 34.7 35.7 33.5 33.8 34.6 34.5
```

4 Comments

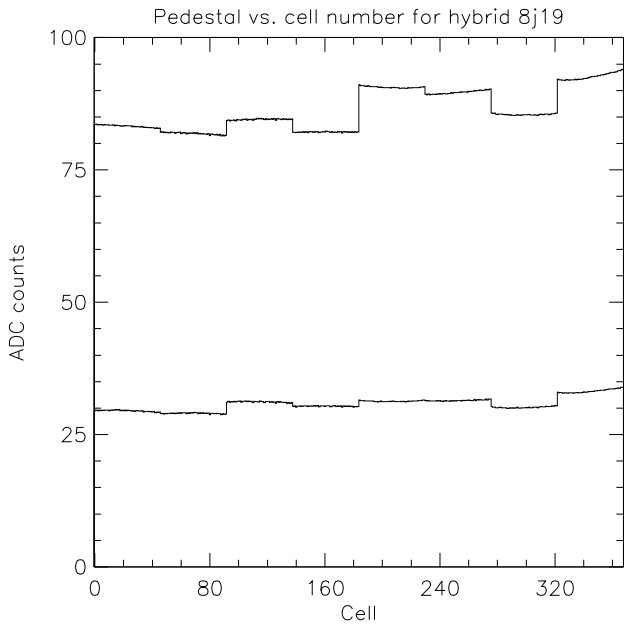
No comments.

5 Pedestal

The following plot shows the pedestals vs. channel number with and without charge injection:



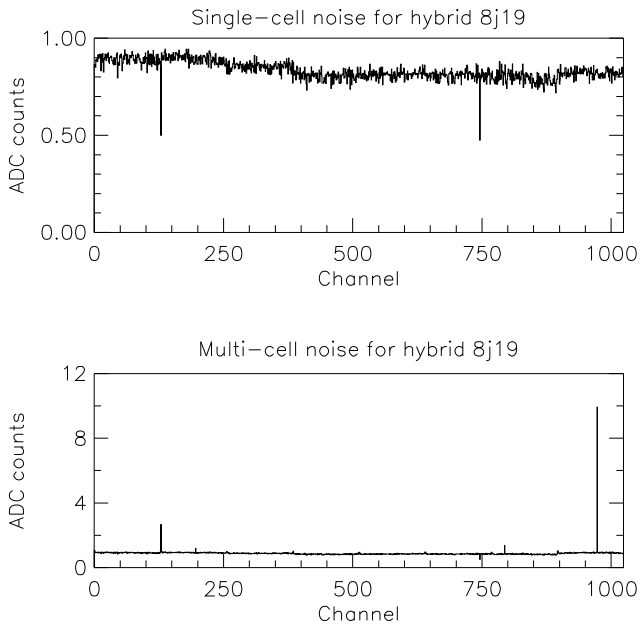
The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.



6 Noise

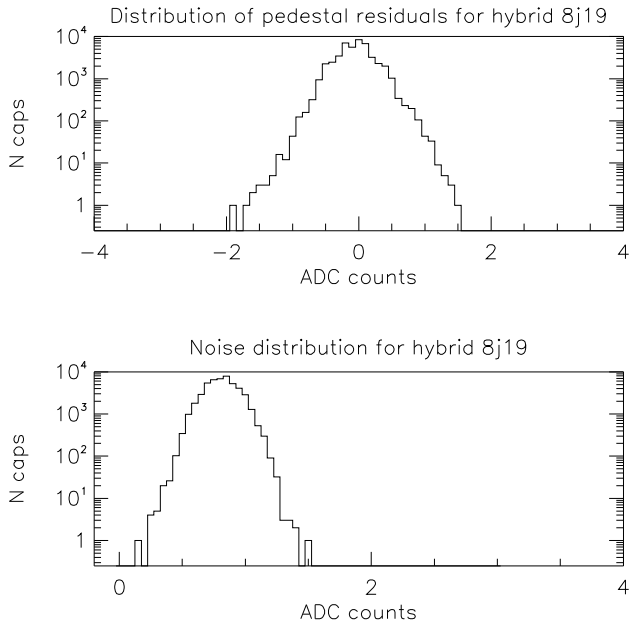
The following plots show the single-cell and multi-cell RMS noise:

RMS Noise

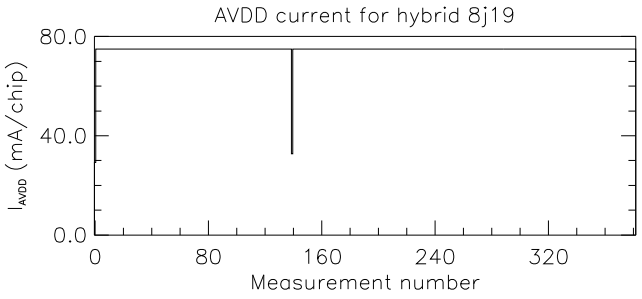
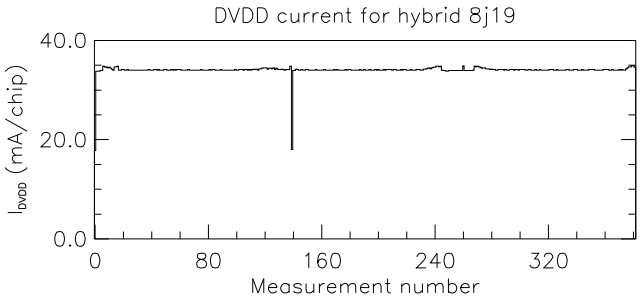


The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise



7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in