Hybrid 8j257 Traveler

June 19, 2000

1 Connectivity

Repairs on shorts:
Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 8J257 has die: 173-061 173-066 173-067 173-073 175-112 175-114 175-116 175-117

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

```
% Hybrid 8j257 classification info. Created 08:54:09 06/19/00

Hybrid 8j257 quality: fair
Chip classification: good good good fair good good good fair
Bad channels (chip-channel): 3-98
Bad cells: none
Random bad capacitors (chip-cell-channel): 3-16-11 3-38-30 3-43-104
   7-30-28 7-30-29
Channels at pedestal needed for DCMS: 30.6 30.6 34.6 35.7 37.5 37.7 36.5 34.1
```

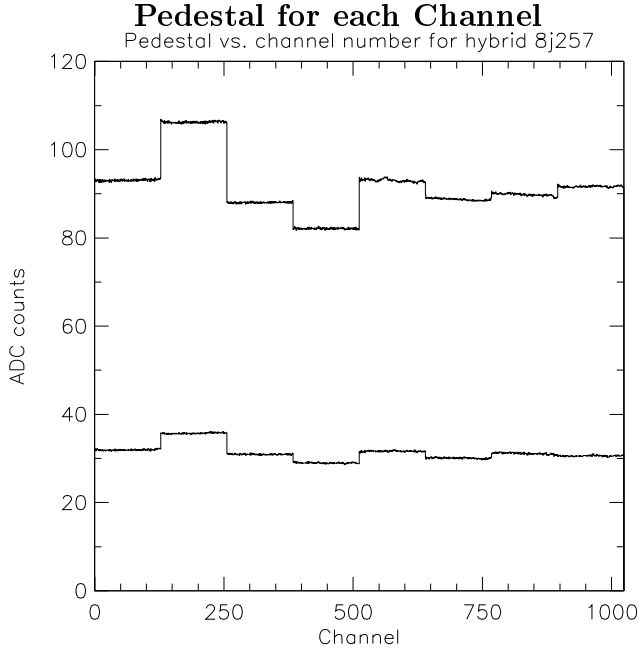
4 Comments

HV tested.

5 Pedestal

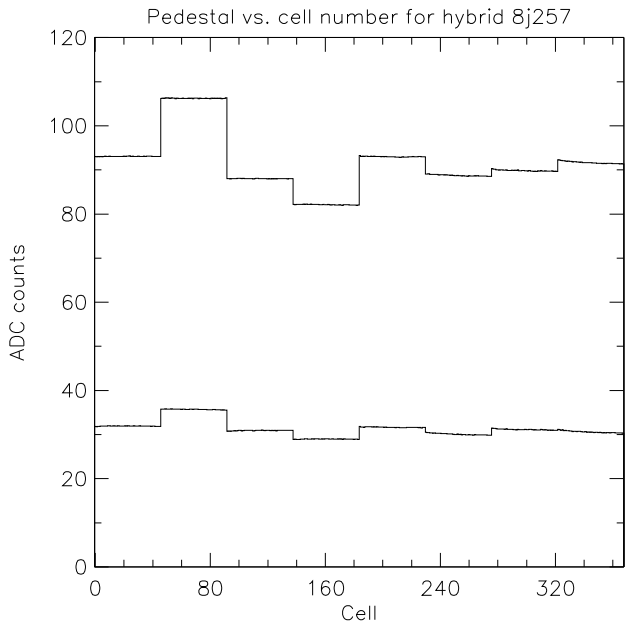
The following plot shows the pedestals vs. channel number with and without charge injection:

Pedestal for each Channel



The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.

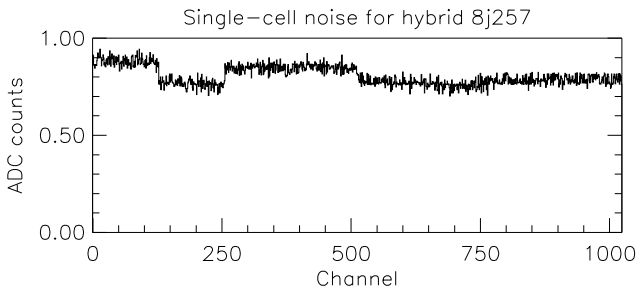
Pedestal vs. Pipeline Cell ID

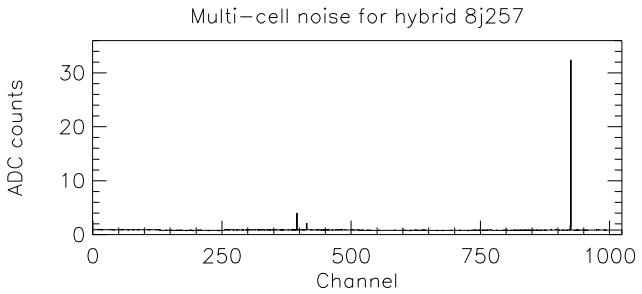


6 Noise

The following plots show the single-cell and multi-cell RMS noise:

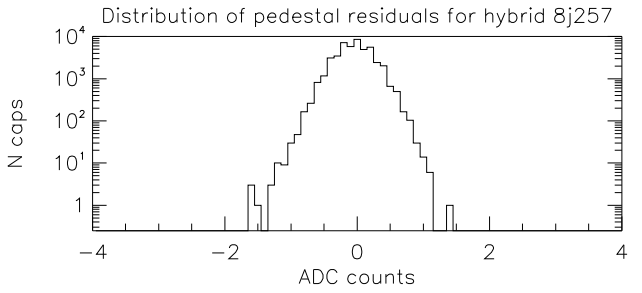
RMS Noise

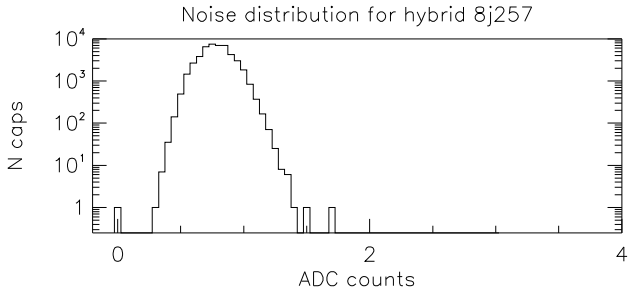




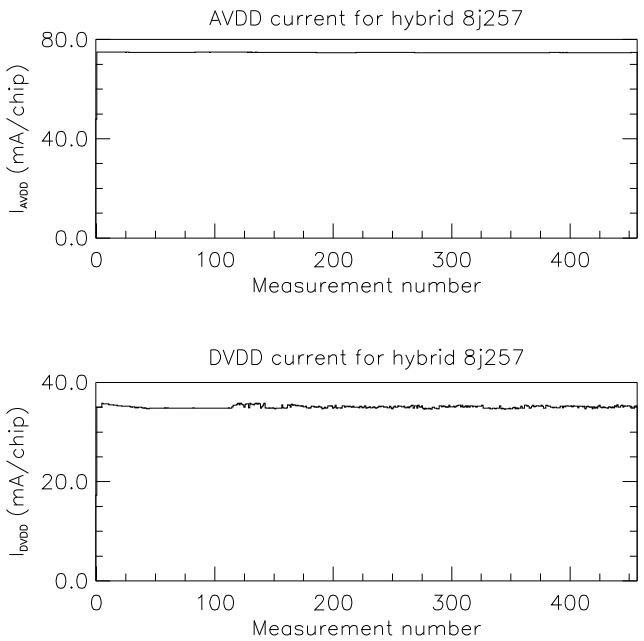
The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise





7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in