Hybrid 8j400 Traveler

July 13, 2000

1 Connectivity

Repairs on shorts:
Fixes for opens:

2 Chips

The following chips were assembled on this hybrid (in the order that the chips get read out):
Hybrid 8J400 has die: 000-000 000-000 000-000 000-000 000-000 000-000 000-000 000-000

3 Data Analysis Summary

The following summarizes the findings of the Burn-in data analysis program. The first chip in the readout name is numbered 0.

```
% Hybrid 8j400 classification info. Created 14:59:22 07/12/00

Hybrid 8j400 quality: fair
Chip classification: good fair fair fair good good good fair
Bad channels (chip-channel): 1-113 2-46 3-121
Bad cells: none
Random bad capacitors (chip-cell-channel): 1-0-90 1-14-90 1-15-90
   1-18-90 1-19-90 1-23-90 1-28-90 1-33-90 1-35-90 1-4-90 1-44-90
   1-7-90 3-1-123 7-39-112
Channels at pedestal needed for DCMS: 31.6 31.5 32.5 31.6 31.8 32.3 34.3 31.6
```

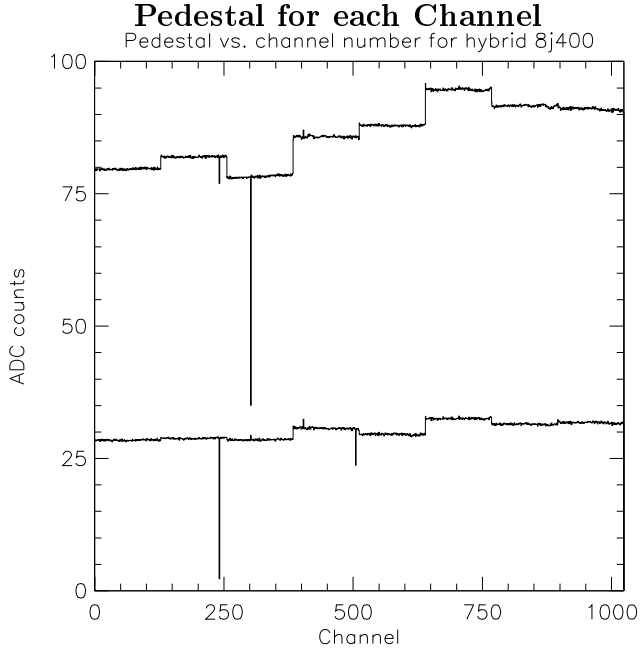
4 Comments

HV tested.

5 Pedestal

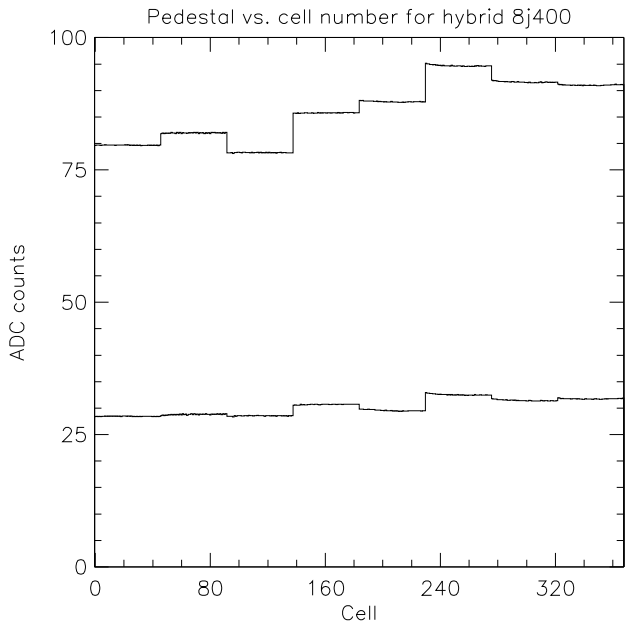
The following plot shows the pedestals vs. channel number with and without charge injection:

Pedestal for each Channel



The following plot shows the average pedestal vs. pipeline cell ID with and without charge injection. The x-axis is cell ID offset by 46 for each chip. Thus cell ID 5 for chip 1 is at x=5, chip 2 at x=51, chip 3 at x=97, etc.

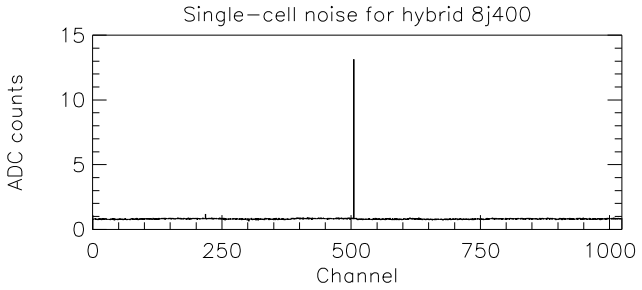
Pedestal vs. Pipeline Cell ID

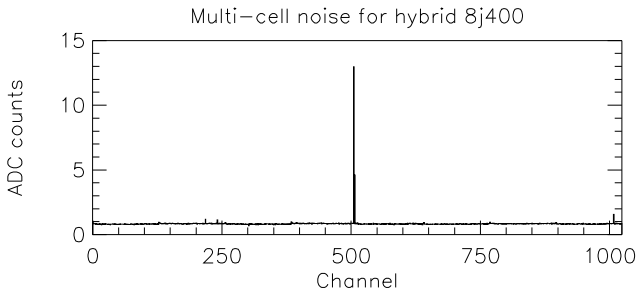


6 Noise

The following plots show the single-cell and multi-cell RMS noise:

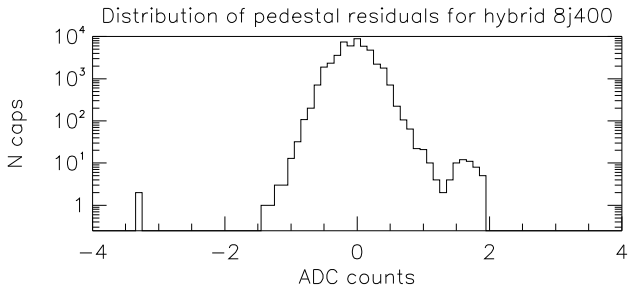
RMS Noise

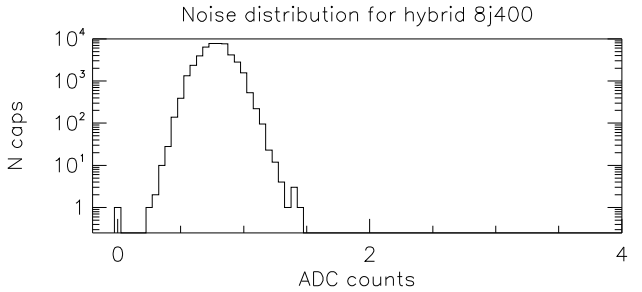




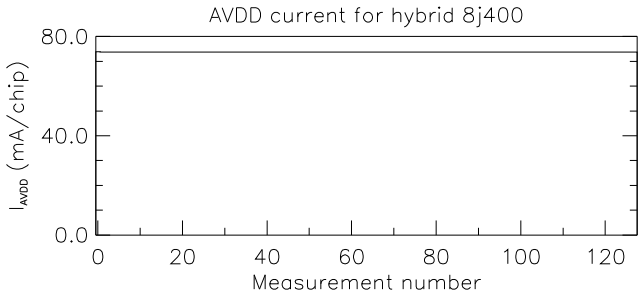
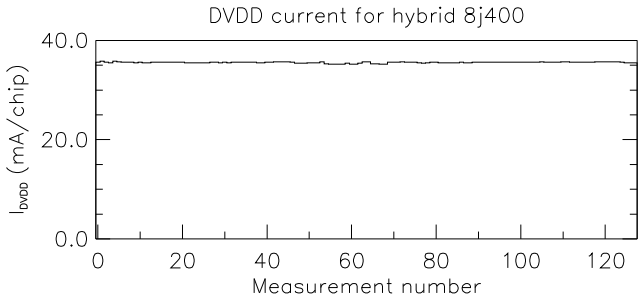
The following are histograms of the pedestal of each pipeline capacitor relative to the chip average (top); and the RMS noise of each capacitor (bottom):

Distribution of Pedestals and Noise





7 Currents

The following plots show the supply currents per chip as a function of measurment number. Currents are measured approximately every 10 minutes:

Supply Currents During Burn-in