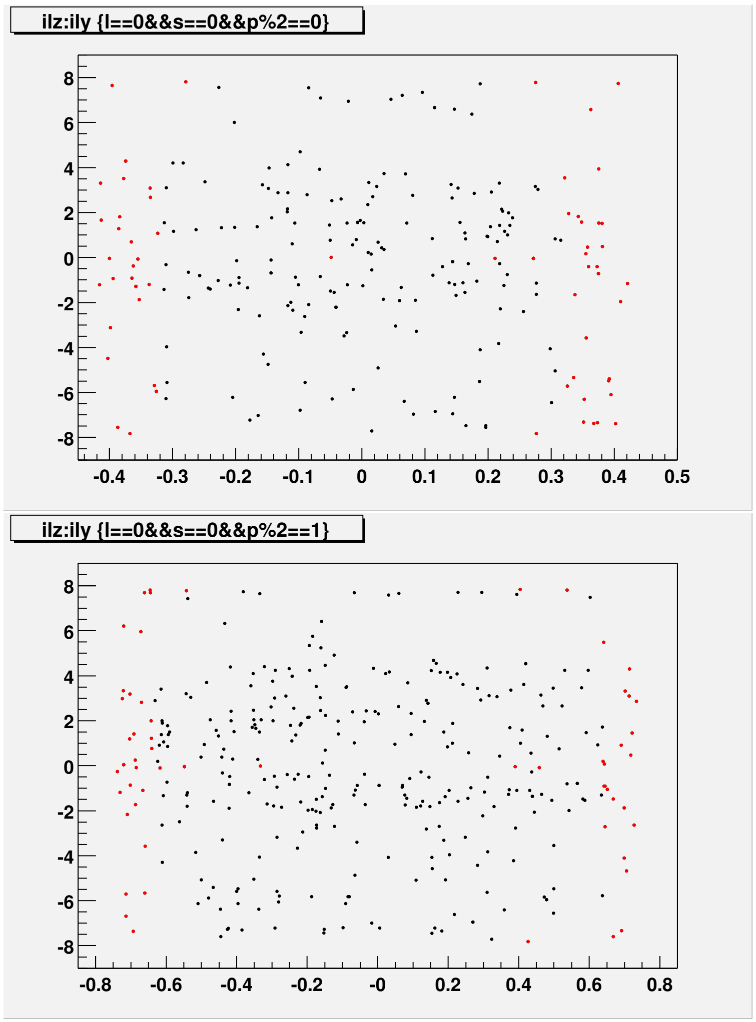
L00 Bug in 3.18.0int3–3.18.0int6



- There was a bug that declared the central region of each L00 ladder to be inactive.

- As of `ver 3.18.0int6` this has been fixed

SiGeometry Status

- As of `ver 3.18.0int7`, the SiliconGeometry is more or less finalized
- Next minor change will be in definition of wafer volumes to more correctly take into account particles in MC that pass through both an active and inactive region